

MAR 26 2004

Sheet 1 of 2

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Form PTO-1449 U.S. Department of Commerce Patent and Trademark Office TRADEMARK INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)								Atty. Docket No. P24677	Serial No. Unknown	1074824L
								Applicant Ryutaro YAMANAKA et al		
								Filing Date December 31, 2003	Group Unknown	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DD	6	1	9	5	7	8	2	02/27/01	RAHMATULLAH et al			
DD	6	2	5	7	7	5	6	07/10/01	ZARUBINSKY et al			
DD	6	4	7	7	6	6	1	11/03/03	YAMANAKA			
DD	5	6	3	3	8	9	7	05/27/97	FETTWEIS et al			
DD	5	9	2	3	7	1	3	07/1999	HATAKEYAMA			
DD	5	3	2	7	4	4	0	07/1999	FREDRICKSON et al			
DD	5	9	1	2	9	0	8	06/1999	CESARI et al			
DD	5	9	8	7	4	9	0	11/1993	ALIDINA et al			
DD	5	2	2	0	5	7	0	06/1993	LOU et al			
DD	5	4	1	4	7	3	8	05/1995	BIENZ			
DD	5	7	1	0	7	8	4	01/1998	KINDRED et al			
DD	5	0	2	7	3	7	4	01/1991	ROSSMAN			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
0	9	-	1	4	8	9	4	3	06/06/97	JAPAN		X
5	3	-	2	1	5	2	2	7	09/07/88	JAPAN		X
6	6	-	1	6	4	4	2		06/10/94	JAPAN		
	8	-	8	4	0	8	2		09/14/94	JAPAN		
	9	-	1	4	8	9	4	3	11/17/95	JAPAN		
1	0	-	1	0	7	6	5	1	04/24/98	JAPAN		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	1	English Language Abstract JP 09-148943.
	2	English Language Abstract JP 63-215227.
	3	UEDA et al., "A 16-bit Digital Signal Processor with Specially Arranged Multiply-Accumulator for Low Power Consumption", IEICE Trans. Electron., Vol. E78-C, No. 12, December 1995.

EXAMINER C	DATE CONSIDERED 6/1/05
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

MR

10-17-05